

ADMISSIBILITY CRITERIA FOR A HIERARCHICAL DESIGN OF HYBRID CONTROL SYSTEMS¹

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Abstract: Many hybrid control problems of practical interest can be decomposed in a hierarchy of control objectives, where each objective refers to a particular time scale and to a particular level of measurement aggregation. It is common engineering practice to exploit this hierarchical structure in the development of ad hoc solutions to hybrid control problems that are far beyond the computational limitations of known methods for systematic hybrid system design. This paper extends a known design method to (i) benefit from hierarchical decompositions provided by engineering intuition, and to (ii) allow for a formal proof that the composition of the individual layers forms an overall solution. *Copyright © 2003 IFAC*

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1. INTRODUCTION

In the basic hybrid control configuration, continuous dynamics interact with a discrete event supervisor via a suitable interface that mediates between discrete and continuous signals. A widely accepted closed-loop model for this configuration are so called *hybrid automata* (Alur *et al.*, 2000; Henzinger, 1996). In this paper, we take the perspective of the supervisor and summarise the remaining entities as the *hybrid plant*. Control specifications are formalised as languages over the alphabet of external discrete events, and the task of the supervisor is to enforce that the closed loop evolves on acceptable trajectories according to the specification. A crucial feature of the hybrid setting is that state machine realisations of the plant typically evolve on a real-valued vector, and hence uncountable, state space. The core idea of abstraction-

based approaches is that, rather than synthesising a supervisor for the actual plant behaviour, one works instead with a plant abstraction that can be realised by a finite automaton (Koutsoukos *et al.*, 2000; Cury *et al.*, 1998; Lunze *et al.*, 1997; Philips *et al.*, 1999). In (Moor and Raisch, 1999; Moor *et al.*, 2002), we develop an abstraction-based synthesis procedure within Willems' behavioural systems theory. Here, the hybrid plant is represented by its *external behaviour*, defined as the set \mathfrak{B}_p of all sequences of pairs of input and output events that are compatible with the hybrid plant dynamics. Our key result is a synthesis procedure that solves the original control problem via a *plant abstraction* \mathfrak{B}_{ca} with $\mathfrak{B}_{ca} \supseteq \mathfrak{B}_p$.

Although the considered abstractions \mathfrak{B}_{ca} are internally based on state aggregation, they are defined on the same signal space and refer to the same time scale as the original plant. On the other hand, many applications suggest an obvious decomposition of the overall control problem in a number of subproblems that refer to a hierarchy of time scales and measurement

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aggregations. It is common engineering practice to use this type of decomposition to find ad hoc solutions that are far beyond the computational limitations of known systematic design methods. In this paper, we extend our previous work by additional layers between plant and supervisor to represent a hierarchy of time scales, measurement aggregations, subproblems and their solutions. We develop a method that is grounded in the engineering intuition used for the hierarchical ad hoc design of hybrid systems, and additionally provide a formal proof that the composition of all individual layers solves the original problem.

The framework here is inspired by that of hierarchical DES theory (Wong and Wonham, 1996), but is technically quite distinct because we need to use an input/output structure that adequately represents both time and event driven dynamics for hybrid systems. As in other hierarchical approaches to control (Pappas *et al.*, 2000; Caines and Wei, 1998), we are concerned with the preservation of fundamental properties across levels of abstraction.

The paper is organised as follows. Section 2 summarises key results from (Moor and Raisch, 1999; Moor *et al.*, 2002). In Section 3, we present a two-level design that is readily shown to enforce the specification. The question whether the composed overall system satisfies standard admissibility conditions is more subtle. We develop sufficient criteria in Sections 4 and 5 for quasi-continuous low-level control and measurement aggregation, respectively. Section 6 extends the results to a multi-level configuration.

2. ABSTRACTION-BASED SUPERVISORY CONTROL

The purpose of this section is to briefly summarise key results of our earlier work (Moor and Raisch, 1999; Moor *et al.*, 2002) in abstraction-based supervisory controller synthesis for hybrid systems within Willems' behavioural systems theory (Willems, 1991).

Willems defines the behaviour of a dynamical system as the set of all trajectories on which the system can possibly evolve. In this paper, we restrict our considerations to the discrete-times axis \mathbb{N}_0 :

Definition 1. A behaviour \mathfrak{B} over a signal space W is a set of maps $w: \mathbb{N}_0 \rightarrow W$; i.e. $\mathfrak{B} \subseteq W^{\mathbb{N}_0}$.²

The external plant behaviour $\mathfrak{B}_p \subseteq W^{\mathbb{N}_0}$ is defined as the set of all event sequences on which the hybrid plant can evolve in open loop. In (Moor *et al.*, 2001), we carefully derive \mathfrak{B}_p for a broad class of hybrid systems based on the *hybrid automata* model (Alur *et al.*, 2000; Henzinger, 1996) and observe that \mathfrak{B}_p

inherits the input/output structure from the underlying continuous dynamics; i.e. we have $W := U \times Y$ and \mathfrak{B}_p conforms to a slightly weakened version of Willems' *I/O behaviours*:

Definition 2. A behaviour $\mathfrak{B} \subseteq W^{\mathbb{N}_0}$ to said to be a (strict) *I/- behaviour* w.r.t. (U, Y) , if³

- (i) the *input is free*, i.e. $\mathcal{P}_U \mathfrak{B} = U^{\mathbb{N}_0}$ and
- (ii) the *output does (strictly) not anticipate the input*, i.e.

$$\mathcal{P}_U \tilde{w}|_{[0,k]} = \mathcal{P}_U \hat{w}|_{[0,k]} \Rightarrow (\exists w \in \mathfrak{B})[$$

$$\mathcal{P}_Y w|_{[0,k]} = \mathcal{P}_Y \tilde{w}|_{[0,k]} \text{ and } \mathcal{P}_U w = \mathcal{P}_U \hat{w}]$$
 for all $k \in \mathbb{N}_0$, $\tilde{w}, \hat{w} \in \mathfrak{B}$; for the *strict* case the premiss on the l.h.s. is weakened to $\mathcal{P}_U \tilde{w}|_{[0,k]} = \mathcal{P}_U \hat{w}|_{[0,k]}$.

Adapting the concepts of supervisory control theory for DESs (Ramadge and Wonham, 1989) to the behavioural framework, the task of a supervisor $\mathfrak{B}_{\text{sup}} \subseteq W^{\mathbb{N}_0}$ is to restrict a plant behaviour $\mathfrak{B}_p \subseteq W^{\mathbb{N}_0}$ so that the closed loop is guaranteed to evolve only on acceptable signals $\mathfrak{B}_{\text{spec}} \subseteq W^{\mathbb{N}_0}$. The closed-loop behaviour is defined by $\mathfrak{B}_{\text{cl}} := \mathfrak{B}_p \cap \mathfrak{B}_{\text{sup}}$ and $\mathfrak{B}_{\text{sup}}$ is said to *enforce the specification* if $\mathfrak{B}_{\text{cl}} \subseteq \mathfrak{B}_{\text{spec}}$.

In examining the notion of *I/- behaviours*, we identify two *admissibility criteria* for the interconnection of plant and supervisor: (i) any restrictions on the plant output shall only be imposed indirectly by restricting the plant input; and (ii) at any time there must be possible future evolution. Formally, we state:

Definition 3. A supervisor $\mathfrak{B}_{\text{sup}} \subseteq W^{\mathbb{N}_0}$ is *admissible* to the plant $\mathfrak{B}_p \subseteq W^{\mathbb{N}_0}$ if

- (i) $\mathfrak{B}_{\text{sup}}$ is *generically implementable*, i.e. $k \in \mathbb{N}_0$, $w|_{[0,k]} \in \mathfrak{B}_{\text{sup}}|_{[0,k]}$, $\tilde{w}|_{[0,k]} \in W^{k+1}$, $\tilde{w}|_{[0,k]} \approx_y w|_{[0,k]}$ implies $\tilde{w}|_{[0,k]} \in \mathfrak{B}_{\text{sup}}|_{[0,k]}$; and
- (ii) \mathfrak{B}_p and $\mathfrak{B}_{\text{sup}}$ are *non-conflicting*, i.e. $\mathfrak{B}_p|_{[0,k]} \cap \mathfrak{B}_{\text{sup}}|_{[0,k]} = (\mathfrak{B}_p \cap \mathfrak{B}_{\text{sup}})|_{[0,k]}$ for all $k \in \mathbb{N}_0$.

This leads to the following formulation of the problem of supervisory control.

Definition 4. Given a plant $\mathfrak{B}_p \subseteq W^{\mathbb{N}_0}$, $W = U \times Y$, and a specification $\mathfrak{B}_{\text{spec}} \subseteq W^{\mathbb{N}_0}$, the pair $(\mathfrak{B}_p, \mathfrak{B}_{\text{spec}})_{\text{cp}}$ is a *supervisory control problem*. A supervisor $\mathfrak{B}_{\text{sup}} \subseteq W^{\mathbb{N}_0}$ that is admissible to \mathfrak{B}_p and that enforces $\mathfrak{B}_{\text{spec}}$ is said to be a *solution* of $(\mathfrak{B}_p, \mathfrak{B}_{\text{spec}})_{\text{cp}}$.

³ The *restriction* operator $(\cdot)|_{[k_1, k_2]}$ maps sequences $w \in W^{\mathbb{N}_0}$ to finite strings $w|_{[k_1, k_2]} := w(k_1)w(k_1+1) \cdots w(k_2-1) \in W^{k_2-k_1}$, where we use $W^0 := \{\epsilon\}$ and ϵ denotes the *empty string*. For closed intervals, the operator $(\cdot)|_{[k_1, k_2]}$ is defined accordingly. For $W = U \times Y$, we denote \mathcal{P}_U and \mathcal{P}_Y the *natural projection* operators to the respective component, i.e. $\mathcal{P}_U w = u$ and $\mathcal{P}_Y w = y$ for $w = (u, y)$, $u \in U^{\mathbb{N}_0}$, $y \in Y^{\mathbb{N}_0}$. We use $\tilde{w}|_{[0,k]} \approx_y w|_{[0,k]}$ as an abbreviation for the two strings to be identical up to the last output event, i.e. $\mathcal{P}_U \tilde{w}|_{[0,k]} = \mathcal{P}_U w|_{[0,k]}$ and $\mathcal{P}_Y \tilde{w}|_{[0,k]} = \mathcal{P}_Y w|_{[0,k]}$.

² \mathbb{N} denotes the positive integers and $\mathbb{N}_0 := \mathbb{N} \cup \{0\}$. The set of all sequences in W is denoted $W^{\mathbb{N}_0} := \{w: \mathbb{N}_0 \rightarrow W\}$.

If both \mathfrak{B}_p and $\mathfrak{B}_{\text{spec}}$ were realised by finite automata, the *least restrictive solution* of $(\mathfrak{B}_p, \mathfrak{B}_{\text{spec}})_{\text{cp}}$ could be readily computed. While a finite automaton realisation for $\mathfrak{B}_{\text{spec}}$ is a modest requirement, the hybrid plant in general is *not* realisable on a finite state space. We approach the problem by replacing \mathfrak{B}_p with an *abstraction* \mathfrak{B}_{ca} (so $\mathfrak{B}_p \subseteq \mathfrak{B}_{\text{ca}}$) that is realised by a finite automaton; so we can establish a solution $\mathfrak{B}_{\text{sup}}$ of $(\mathfrak{B}_{\text{ca}}, \mathfrak{B}_{\text{spec}})_{\text{cp}}$. Clearly, $\mathfrak{B}_{\text{sup}}$ enforces the specification for the original plant: $\mathfrak{B}_p \cap \mathfrak{B}_{\text{sup}} \subseteq \mathfrak{B}_p \cap \mathfrak{B}_{\text{ca}} \subseteq \mathfrak{B}_{\text{spec}}$. An argument that shows that $\mathfrak{B}_{\text{sup}}$ also is admissible to \mathfrak{B}_p can be based on the following notion of *completeness*:

Definition 5. A behaviour $\mathfrak{B} \subseteq W^{\mathbb{N}_0}$ is *complete* if
 $w \in \mathfrak{B} \Leftrightarrow \forall k \in \mathbb{N}_0 : w|_{[0,k]} \in \mathfrak{B}|_{[0,k]}$.

By the following proposition, admissibility of a supervisor is independent of the particular plant dynamics provided that all involved behaviours are complete.

Proposition 6. Let $\mathfrak{B}_p \subseteq W^{\mathbb{N}_0}$ be a complete I/- behaviour and $\mathfrak{B}_{\text{sup}} \subseteq W^{\mathbb{N}_0}$ be complete and generically implementable. Then \mathfrak{B}_p and $\mathfrak{B}_{\text{sup}}$ are non-conflicting.

For the rest of this paper, we restrict consideration to complete behaviours, and we obtain our main result for abstraction-based supervisory control as a consequence of Proposition 6.

Theorem 7. Let $\mathfrak{B}_{\text{ca}} \subseteq W^{\mathbb{N}_0}$, $W = U \times Y$, be an abstraction of an I/- behaviour $\mathfrak{B}_p \subseteq W^{\mathbb{N}_0}$, let $\mathfrak{B}_{\text{spec}} \subseteq W^{\mathbb{N}_0}$, and let $\mathfrak{B}_{\text{sup}} \subseteq W^{\mathbb{N}_0}$ be a solution to the supervisory control problem $(\mathfrak{B}_{\text{ca}}, \mathfrak{B}_{\text{spec}})_{\text{cp}}$. If \mathfrak{B}_p and $\mathfrak{B}_{\text{sup}}$ are complete then $\mathfrak{B}_{\text{sup}}$ is a solution of $(\mathfrak{B}_p, \mathfrak{B}_{\text{spec}})_{\text{cp}}$.

3. A TWO-LEVEL BOTTOM-UP DESIGN

We motivate our approach with a mobile robot scenario, in which a robot shall patrol some area. Suppose we are given the robot behaviour \mathfrak{B}_p^l over a signal space $W_L = U_L \times Y_L$, where U_L represents low-level inputs for acceleration and Y_L represents velocity and position. The control objective can be represented by the set $\mathfrak{B}_{\text{spec}}^l \subseteq W_L^{\mathbb{N}_0}$ of all signals that correspond to some motion that we regard as an acceptable patrolling behaviour, e.g. we may partition the area and consider all paths as acceptable that pass through the partition blocks in a cyclic fashion. This leads to the control problem $(\mathfrak{B}_p^l, \mathfrak{B}_{\text{spec}}^l)_{\text{cp}}$ and we seek a solution $\mathfrak{B}_{\text{sup}}^l \subseteq W_L^{\mathbb{N}_0}$. However, a reasonably accurate plant model \mathfrak{B}_p^l will be based on the mechanics of the robot, and it appears impractical to solve $(\mathfrak{B}_p^l, \mathfrak{B}_{\text{spec}}^l)_{\text{cp}}$ “in one go”. Engineering intuition suggests that we first design a family of low-level controllers that implement elementary manoeuvres like “move forward”,

“turn right”, “turn left”. In a second design step, we ask for a high-level supervisor to schedule the elementary manoeuvres to achieve the desired patrolling behaviour. The high-level supervisor $\mathfrak{B}_{\text{sup}}^h \subseteq W_H^{\mathbb{N}_0}$ operates on a high-level signal space $W_H = U_H \times Y_H$, where each control action in U_H selects a particular low-level controller and the measurement events in Y_H correspond to a coarse quantisation of the robot position, perhaps based on the partition blocks used for the statement of $\mathfrak{B}_{\text{spec}}^l$.

The relationship between low-level and high-level signals is formally represented by a behaviour \mathfrak{B}_{im} over $W_H \times W_L = U_H \times Y_H \times U_L \times Y_L$; see Figure 1. Note that the behaviour \mathfrak{B}_{im} may itself exhibit nontrivial dynamics and therefore provides a universal tool to link the two levels.

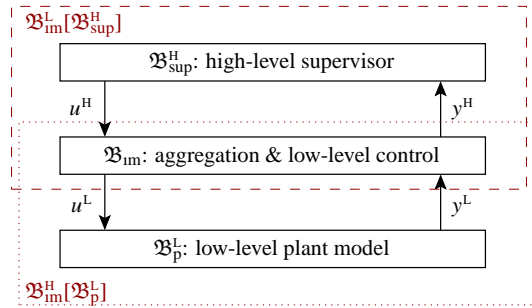


Fig. 1. Plant (supervisor) perspective, dashed (dotted)

From the perspective of the low-level plant \mathfrak{B}_p^l , the interconnection of \mathfrak{B}_{im} with $\mathfrak{B}_{\text{sup}}^h$ plays the role of a compound low-level supervisor $\mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^h]$ over W_L , as indicated by the dashed box in Figure 1. The external behaviour $\mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^h]$ is given by the projection of \mathfrak{B}_{im} into $W_L^{\mathbb{N}_0}$ with the internal high-level signal restricted to $\mathfrak{B}_{\text{sup}}^h$:

$$\mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^h] := \{w^l \mid (\exists w^h \in \mathfrak{B}_{\text{sup}}^h)[(w^h, w^l) \in \mathfrak{B}_{\text{im}}]\}. \quad (1)$$

In addressing the original problem $(\mathfrak{B}_p^l, \mathfrak{B}_{\text{spec}}^l)_{\text{cp}}$, we require that $\mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^h]$ solves $(\mathfrak{B}_p^l, \mathfrak{B}_{\text{spec}}^l)_{\text{cp}}$, and, in particular, enforces the specification:

$$\mathfrak{B}_p^l \cap \mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^h] \subseteq \mathfrak{B}_{\text{spec}}^l. \quad (2)$$

From the perspective of the high-level supervisor $\mathfrak{B}_{\text{sup}}^h$, we obtain from interconnecting \mathfrak{B}_{im} with \mathfrak{B}_p^l a compound high-level plant $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^l]$ over W_H (dotted box in Figure 1):

$$\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^l] := \{w^h \mid (\exists w^l \in \mathfrak{B}_p^l)[(w^h, w^l) \in \mathfrak{B}_{\text{im}}]\}. \quad (3)$$

Although we are not given a high-level specification, we still need to fulfil the admissibility criteria for system interconnection: $\mathfrak{B}_{\text{sup}}^h$ must be generically implementable, and $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^l]$ and $\mathfrak{B}_{\text{sup}}^h$ must be non-conflicting. We summarise our discussion of Figure 1:

Definition 8. The pair $(\mathfrak{B}_{\text{im}}, \mathfrak{B}_{\text{sup}}^h)_{\text{tl}}$ is a *two-level hierarchical solution* to the supervisory control problem $(\mathfrak{B}_p^l, \mathfrak{B}_{\text{spec}}^l)_{\text{cp}}$ if

- (i) $\mathfrak{B}_p^L \cap \mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^H] \subseteq \mathfrak{B}_{\text{spec}}^L$, and
- (ii) $\mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^H]$ is admissible to \mathfrak{B}_p^L , and
- (iib) $\mathfrak{B}_{\text{sup}}^H$ is admissible to $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^L]$.

We are now in the position to recover the intuitive bottom-up-design motivated by the mobile robot scenario. In a first step, we represent the *intended* relationship between high-level signals and low-level signals. Formally, let $\mathfrak{B}_{\text{spec}}^{\text{HL}} \subseteq (W_H \times W_L)^{\mathbb{N}_0}$ denote the set of all signal pairs (w^H, w^L) that conform with the desired effect of high-level control actions on the low-level plant \mathfrak{B}_p^L , and the desired scheme of measurement aggregation to generate high-level measurement events from low-level signals. We then ask the intermediate layer \mathfrak{B}_{im} to enforce the specification $\mathfrak{B}_{\text{spec}}^{\text{HL}}$ when interconnected with the low-level plant \mathfrak{B}_p^L . This condition is expressed by the following inclusion:

$$\{(w^H, w^L) \in \mathfrak{B}_{\text{im}} \mid w^L \in \mathfrak{B}_p^L\} \subseteq \mathfrak{B}_{\text{spec}}^{\text{HL}}. \quad (4)$$

Suppose we have designed \mathfrak{B}_{im} according to Eq. (4) and, in a second step, we want to design $\mathfrak{B}_{\text{sup}}^H$. Thus, we are looking for an abstraction $\tilde{\mathfrak{B}}_p^H$ of the high-level plant $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^L]$ and for a high-level specification $\tilde{\mathfrak{B}}_{\text{spec}}^H$ that expresses $\mathfrak{B}_{\text{spec}}^L$ in terms of high-level signals. Both can be obtained from Eq. (4):

$$\tilde{\mathfrak{B}}_p^H := \{w^H \mid (\exists w^L) [(w^H, w^L) \in \mathfrak{B}_{\text{spec}}^{\text{HL}}]\}; \quad (5)$$

$$\tilde{\mathfrak{B}}_{\text{spec}}^H := \{w^H \mid (\forall w^L) [(w^H, w^L) \in \mathfrak{B}_{\text{spec}}^{\text{HL}} \Rightarrow w^L \in \mathfrak{B}_p^L]\}. \quad (6)$$

Observe that the control problem $(\tilde{\mathfrak{B}}_p^H, \tilde{\mathfrak{B}}_{\text{spec}}^H)_{\text{cp}}$ does *not* depend on the actual low-level plant under low-level control $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^L]$, but only on the intended outcome $\mathfrak{B}_{\text{spec}}^{\text{HL}}$ of the preceding low-level design. In our motivational robot scenario, $\mathfrak{B}_{\text{spec}}^{\text{HL}}$ may be modelled by a *linear hybrid automata* (Alur *et al.*, 2000) in which a two dimensional polyhedral differential inclusion specifies constraints on the continuous evolution of the robot's position, *reset relations* abstract motion during the settling time of the low-level controllers, and *mode invariants* correspond to the measurement aggregation in that the high-level supervisor is only notified of discrete *mode transitions*. A high-level supervisor $\mathfrak{B}_{\text{sup}}^H$ that solves $(\tilde{\mathfrak{B}}_p^H, \tilde{\mathfrak{B}}_{\text{spec}}^H)_{\text{cp}}$ can then be computed efficiently, e.g. using the methods presented in (Moor and Raisch, 1999; Moor *et al.*, 2002).

In general, the choice of $\mathfrak{B}_{\text{spec}}^{\text{HL}}$ can be guided by the same engineering intuition that we would use in a hierarchical ad hoc design. The contribution here is to develop a framework in which we can formally prove that the composition of a high-level controller with an intermediate layer forms a solution of the original problem. Proposition 9 provides a first step in this proof and shows that a high-level supervisor design based on $(\tilde{\mathfrak{B}}_p^H, \tilde{\mathfrak{B}}_{\text{spec}}^H)_{\text{cp}}$ satisfies requirement (i) in Definition 8: the overall configuration enforces the low-level specification. In the following sections, we address the admissibility criteria (ii) and (iib).

Proposition 9. Any solution $\mathfrak{B}_{\text{sup}}^H$ of $(\tilde{\mathfrak{B}}_p^H, \tilde{\mathfrak{B}}_{\text{spec}}^H)_{\text{cp}}$ satisfies $\mathfrak{B}_p^L \cap \mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^H] \subseteq \mathfrak{B}_{\text{spec}}^L$.

4. ADMISSIBILITY: UNIFORM TIME SCALES

What properties should we ask for \mathfrak{B}_{im} in order to satisfy the admissibility criteria in Definition 8? In this section, we examine the case of *quasi-continuous* controllers, i.e. controllers that have been designed by continuous methods but are technically realised by digital hardware at a reasonably high sampling rate and a comparatively fine quantisation. An important feature of this setting is a uniform time scale on all signals, e.g. $y^L(k)$ takes place at the same physical time as $y^H(k)$.

Natural candidates for \mathfrak{B}_{im} are *I/-* behaviours, where u^H and y^L play the role of the input to \mathfrak{B}_{im} while y^H and u^L are considered outputs. In addition, we require that \mathfrak{B}_{im} and \mathfrak{B}_p^L be complete, and from this conclude that the *I/-* property of \mathfrak{B}_p^L is passed on to $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^L]$. In order to derive completeness of $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^L]$, we require that all signal spaces are finite sets.

Lemma 10. If \mathfrak{B}_{im} is a complete strict *I/-* behaviour w.r.t. $(U_H \times Y_L, Y_H \times U_L)$, and if \mathfrak{B}_p^L is a complete *I/-* behaviour w.r.t. (U_L, Y_L) , then $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^L]$ is a complete *I/-* behaviour w.r.t. (U_H, Y_H) .

The same criteria not only preserves the *I/-* structure of \mathfrak{B}_p^L but also generic implementability of $\mathfrak{B}_{\text{sup}}^H$.

Lemma 11. If \mathfrak{B}_{im} is a complete strict *I/-* behaviour w.r.t. $(U_H \times Y_L, Y_H \times U_L)$, and if $\mathfrak{B}_{\text{sup}}^H$ is complete and generically implementable, then $\mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^H]$ is complete and generically implementable.

Suppose we have found a complete solution $\mathfrak{B}_{\text{sup}}^H$ of $(\tilde{\mathfrak{B}}_p^H, \tilde{\mathfrak{B}}_{\text{spec}}^H)_{\text{cp}}$, and suppose that $\mathfrak{B}_{\text{spec}}^{\text{HL}}$ is implemented by a complete strict *I/-* behaviour \mathfrak{B}_{im} . Under the hypothesis of Lemmata 11 and 10 we conclude together with Proposition 6 that the admissibility criteria (ii) and (iib), Definition 8, are satisfied. Hence, by Proposition 9, the pair $(\mathfrak{B}_{\text{im}}, \mathfrak{B}_{\text{sup}}^H)_{\text{tl}}$ is a two-level hierarchical solution of $(\mathfrak{B}_p^L, \mathfrak{B}_{\text{spec}}^L)_{\text{cp}}$.

5. ADMISSIBILITY: DIFFERENT TIME SCALES

The proposed high-level measurement signal in the robot scenario shall notify the high-level supervisor whenever the robot position passes into a different partition cell. Therefore, high-level and low-level signals refer to different time scales. Furthermore, the relationship between the high-level and low-level timing is *not* determined by a fixed factor but is event driven by the low-level measurement signal. We develop an

internal structure for \mathfrak{B}_{im} that implements this dynamic relationship as a general tool of measurement aggregation.

The following definition extends the basic notion of causal maps (e.g. (Khalil, 1996)) in referring to different time scales for cause and effect, respectively:

Definition 12. Let $F: U^{\mathbb{N}_0} \rightarrow Y^{\mathbb{N}_0}$ and $T: U^{\mathbb{N}_0} \rightarrow \mathbb{N}_0^{\mathbb{N}_0}$. The operator F is said to be *causal* if

$$\tilde{u}|_{[0,k]} = \hat{u}|_{[0,k]} \Rightarrow F(\tilde{u})|_{[0,k]} = F(\hat{u})|_{[0,k]} \quad (7)$$

for all $k \in \mathbb{N}_0$, $\tilde{u}, \hat{u} \in U^{\mathbb{N}_0}$. The operator F is said to be *strictly causal* if

$$\tilde{u}|_{[0,k)} = \hat{u}|_{[0,k)} \Rightarrow F(\tilde{u})|_{[0,k)} = F(\hat{u})|_{[0,k)} \quad (8)$$

for all $k \in \mathbb{N}_0$, $\tilde{u}, \hat{u} \in U^{\mathbb{N}_0}$. The operator T is said to be a *dynamic time scale* if T is strictly causal and if the *time transformation* $T(u): \mathbb{N}_0 \rightarrow \mathbb{N}_0$ is surjective and monotone increasing for all $u \in U^{\mathbb{N}_0}$. The operator F is said to be *causal w.r.t. T* if T is a dynamic time scale and if

$$\tilde{u}|_{[0,j]} = \hat{u}|_{[0,j]} \Rightarrow F(\tilde{u})|_{[0,k]} = F(\hat{u})|_{[0,k]} \quad (9)$$

for $k = T(\tilde{u})(j)$ and all $j \in \mathbb{N}_0$, $\tilde{u}, \hat{u} \in U^{\mathbb{N}_0}$.

For a fixed input u , the time transformation $T(u)$ maps low-level time $j \in \mathbb{N}_0$ to high-level time $k \in \mathbb{N}_0$. By requiring that T itself is a strictly causal operator, we ensure that at any instant of time the transformation $T(u)$ only depends on the strict past of u .

In our target application, the low-level measurement signal y^L plays the role of the input u and drives the time transformation $T(y^L)$. The high-level measurement is generated by $y^H = F(y^L)$ where the operator $F: Y_L^{\mathbb{N}_0} \rightarrow Y_H^{\mathbb{N}_0}$ is required to be causal w.r.t. T . As an example for a realisation of F , consider an automaton that generates high-level events whenever the low-level measurement equals a given value or completes a given cycle.

We relate high-level controls u^H and low-level controls u^L by a sample-and-hold device that is triggered by the time transformation $T(y^L)$; i.e. successive high-level control actions are passed on to the lower level whenever a high-level measurement is generated. Formally, this is expressed by $u^L = u^H \circ T(y^L)$.

In summary, our candidate \mathfrak{B}_{im} is constructed from a dynamic time scale T and an operator F that is causal w.r.t. T . Figure 2 illustrates the formal definition:

$$\mathfrak{B}_{\text{im}} := \{(u^H, y^H, u^L, y^L) \mid y^H = F(y^L) \text{ and } u^L = u^H \circ T(y^L)\}. \quad (10)$$

Our candidate \mathfrak{B}_{im} turns out to be complete:

Proposition 13. Given a dynamic time scale $T: Y_L^{\mathbb{N}_0} \rightarrow \mathbb{N}_0^{\mathbb{N}_0}$ and an operator $F: Y_L^{\mathbb{N}_0} \rightarrow Y_H^{\mathbb{N}_0}$ that is causal w.r.t. T , define \mathfrak{B}_{im} by Eq. (10). Then \mathfrak{B}_{im} is complete.

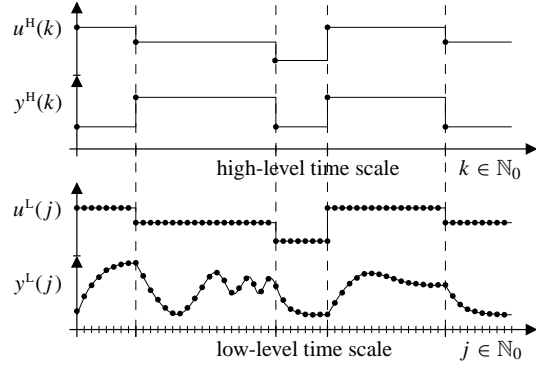


Fig. 2. Relation between time scales in \mathfrak{B}_{im}

Analogous to the results in Section 4, the candidate \mathfrak{B}_{im} from Eq. (10) preserves the input/output structure of a plant and generic implementability of a supervisor.

Lemma 14. Under the hypothesis of Proposition 13, and if \mathfrak{B}_p^L is a complete I/- behaviour w.r.t. (U_L, Y_L) , it follows that $\mathfrak{B}_{\text{im}}^H[\mathfrak{B}_p^L]$ is a complete I/- behaviour w.r.t. (U_H, Y_H) .

Lemma 15. Under the hypothesis of Proposition 13, and provided that $\mathfrak{B}_{\text{sup}}^H$ is complete and generically implementable, it follows that $\mathfrak{B}_{\text{im}}^L[\mathfrak{B}_{\text{sup}}^H]$ is complete and generically implementable.

Along the same line of thought as in the previous section, Lemmata 14 and 15 can be used to show that if the intermediate specification $\mathfrak{B}_{\text{spec}}^H$ is implemented through a behaviour \mathfrak{B}_{im} according to Eq. (10), the pair $(\mathfrak{B}_{\text{im}}, \mathfrak{B}_{\text{sup}}^H)_{\text{tl}}$ is a two-level hierarchical solution of $(\mathfrak{B}_p^L, \mathfrak{B}_{\text{spec}}^L)_{\text{cp}}$.

6. MULTI-LEVEL HIERARCHICAL DESIGN

To treat typical hybrid control configurations, we would like to combine at least two intermediate layers for low-level control and measurement aggregation, respectively. In this section, we show that our results readily extend to a multi-level configuration.

Let $W_i = U_i \times Y_i$ denote the signal space on the i -th level, $0 \leq i \leq m$, and consider a low-level plant \mathfrak{B}_p^0 over W_0 , intermediate layers $\mathfrak{B}_{\text{im}}^{i,i-1}$ over $W_i \times W_{i-1}$, $1 \leq i \leq m$, and a high-level supervisor $\mathfrak{B}_{\text{sup}}^m$ over W_m . We assume that \mathfrak{B}_p^0 is a complete I/- behaviour, that $\mathfrak{B}_{\text{sup}}^m$ is complete and generically implementable, and that each intermediate layer is of either type discussed in Section 4 and 5. For the levels i , $0 \leq i < m$, we iteratively define the behaviour from the plant perspective

$$\mathfrak{B}_{\text{sup}}^i := \{w^i \mid \exists w^{i+1} \in \mathfrak{B}_{\text{sup}}^{i+1} : (w^{i+1}, w^i) \in \mathfrak{B}_{\text{im}}^{i+1,i}\},$$

and, for i , $0 < i \leq m$, from the supervisor perspective:

$$\mathfrak{B}_p^i := \{w^i \mid \exists w^{i-1} \in \mathfrak{B}_p^{i-1} : (w^i, w^{i-1}) \in \mathfrak{B}_{\text{im}}^{i,i-1}\}.$$

Definition 16. The tuple $(\mathfrak{B}_{\text{im}}^{1,0}, \mathfrak{B}_{\text{im}}^{2,1}, \dots, \mathfrak{B}_{\text{im}}^{m,m-1}, \mathfrak{B}_{\text{sup}}^m)_{\text{ml}}$ is said to be an $(m+1)$ -level hierarchical solution to the control problem $(\mathfrak{B}_{\text{p}}^0, \mathfrak{B}_{\text{spec}}^0)_{\text{cp}}$ if

- (i) $\mathfrak{B}_{\text{p}}^0 \cap \mathfrak{B}_{\text{sup}}^0 \subseteq \mathfrak{B}_{\text{spec}}^0$, and
- (ii) for all $i, 0 \leq i \leq m$, $\mathfrak{B}_{\text{sup}}^i$ is admissible to $\mathfrak{B}_{\text{p}}^i$.

We propose an iterative bottom-up design. Assume that, after the design of n layers, we were given behaviours over appropriate signal spaces such that for all $i, 0 < i \leq n$:

- (a) $\mathfrak{B}_{\text{p}}^i \subseteq \tilde{\mathfrak{B}}_{\text{p}}^i$,
- (b) $\{(w^i, w^{i-1}) \in \mathfrak{B}_{\text{im}}^{i,i-1} \mid w^{i-1} \in \tilde{\mathfrak{B}}_{\text{p}}^{i-1}\} \subseteq \mathfrak{B}_{\text{spec}}^{i,i-1}$,
- (c) $w^i \in \mathfrak{B}_{\text{spec}}^i$ and $(w^i, w^{i-1}) \in \mathfrak{B}_{\text{spec}}^{i,i-1} \Rightarrow w^{i-1} \in \mathfrak{B}_{\text{spec}}^{i-1}$.

We design the $(n+1)$ -th layer to satisfy a specification $\mathfrak{B}_{\text{spec}}^{n+1,n}$ over $W_{n+1} \times W_n$. Suppose we can enforce the specification for the plant abstraction $\tilde{\mathfrak{B}}_{\text{p}}^n$; i.e. we find $\mathfrak{B}_{\text{im}}^{n+1,n}$ that satisfies (b) at $i = n+1$. As an abstraction for $\mathfrak{B}_{\text{p}}^{n+1}$ we use $\tilde{\mathfrak{B}}_{\text{p}}^{n+1} := \mathcal{P}_{W_{n+1}} \mathfrak{B}_{\text{spec}}^{n+1,n}$, to satisfy (a) at $i = n+1$. Finally, we choose

$$\mathfrak{B}_{\text{spec}}^{n+1} := \{w^{n+1} \mid (\forall w^n) [(w^{n+1}, w^n) \in \mathfrak{B}_{\text{spec}}^{n+1,n} \Rightarrow w^n \in \mathfrak{B}_{\text{spec}}^n]\}. \quad (11)$$

Clearly our choice of $\mathfrak{B}_{\text{spec}}^{n+1}$ satisfies (c). Hence, all requirements are satisfied at $i = n+1$ and we can iterate the procedure for $n = 1, 2, \dots, m$. As top-level supervisor $\mathfrak{B}_{\text{sup}}^m$ we use a complete solution of $(\tilde{\mathfrak{B}}_{\text{p}}^m, \mathfrak{B}_{\text{spec}}^m)_{\text{cp}}$. It is readily seen that $\mathfrak{B}_{\text{p}}^0 \cap \mathfrak{B}_{\text{sup}}^0 \subseteq \mathfrak{B}_{\text{spec}}^0$ and, hence, the design satisfies condition (i).

To verify condition (ii), we invoke the lemmata from the two previous sections. By successive application of Lemmata 10 and 14 for increasing i , the completeness and the I -property of $\mathfrak{B}_{\text{p}}^i$ is inherited by $\mathfrak{B}_{\text{p}}^{i+1}$. Similarly, by successive application of Lemmata 11 and 15 for decreasing i , the completeness and the generic implementability of $\mathfrak{B}_{\text{sup}}^i$ passes on to $\mathfrak{B}_{\text{sup}}^{i-1}$. By Proposition 6, we conclude that (ii) is satisfied.

7. CONCLUSION

In this paper, we extend the behavioural framework to hybrid system synthesis (Moor and Raisch, 1999; Moor *et al.*, 2002) with additional layers between plant and supervisor to represent a hierarchy of time scales, measurement aggregations, subproblems and their solutions. Technically, our main contribution are sufficient criteria that guarantee standard admissibility conditions for the hierarchical composition of the overall control system.

Although we do account for internal continuous dynamics, all external signals are event sequences. This facilitates the discussion and we can focus our efforts on the hierarchical architecture. We argue that in

engineering realisations of complex control architectures virtually all continuous controllers will be implemented by digital hardware in a quasi-continuous setting. This is covered by our framework, which facilitates the usage of both traditional continuous techniques for time driven dynamics and DES techniques for event driven dynamics.

8. REFERENCES

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